



EXPEDITED PROCEDURE - EXAMINING GROUP 2188

S/N 10/659,920

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| | | | |
|-------------|--------------------------------------|-----------------|------------------|
| Applicant: | Raymond S. Tetrick | Examiner: | Reginald Bragdon |
| Serial No.: | 10/659,920 | Group Art Unit: | 2188 |
| Filed: | September 11, 2003 | Docket No.: | 884.A31US2 |
| Title: | ADAPTIVE PREFETCH OF I/O DATA BLOCKS | | |
| Assignee: | Intel Corporation | Customer No.: | 21186 |

AMENDMENT & RESPONSE UNDER 37 C.F.R. 1.116

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In response to the Final Office Action mailed February 18, 2005, please amend the application as follows:

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4/27/2005